YODA MS2: CONCEPTUAL DESIGN

Our Assistant is Chris Hill

Upload a Supporting document to Vula which has

* Role allocation.
* Division of Labour
* Concept Sketch
* Time Plan(SpreadSheet, preferably)

Our YODA Project

3.8FSG - Function Samples Generator

**Project Description:**

This project involves the storage of a sequence of samples generated algorithmically. Four inputs to the system are used to determine start and end sampling positions, total number of samples as well as the memory address to store the generated samples to.

**RACI Key**

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| --- | --- | --- |
| **Symbol** | **Representation** | **Tasks to be performed** |
| R | Responsible | Those who actually work for the completion of the activity. |
| A | Accountable | The party that is in charge of signing off the work and makes sure that the completed work is in accordance to quality standards. |
| C | Consulted | Those whose opinions and feedback are taken into consideration in order to perform the task. |
| I | Informed | Those that need to be aware of the activity. |

**RACI Chart (Roles and Responsibilities)**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Activity** | **Mr Chris (Project Assistant)** | **Dylan (Team Leader)** | **Takudzwa (Team Member)** | **Sarvesh (Team Member)** |
| Core function Code(Function Samples Generator) | C/I | A/R | C/I | C/I |
| Seven Segment Output Integration | C/I | A/C/I | C/I | R |
| Button/Switch Input Integration | C/I | A/C/I | C/I | R |
| BRAM Navigation | C/I | A/C/I | R | C/I |
| Upload & Test | C/I | A/C/I | R | C/I |
| Memory Optimization | C/I | A/R | C/I | C/I |

Time Plan

|  |  |
| --- | --- |
| **Objective** | **Deadline** |
| Requirements of System   * Core functions defined. * Interacting components identified. * Acceptable Results outlined. * Prototype Specification. | 11 May 2020 |
| System Design   * System block diagram outlined. * Role Allocation. * Necessary resources identified | 22 May 2020 |
| Implementation   * Code implementation * Code Optimisation | 6 June 2020 |
| Testing and Verification   * Seven Segments Output test * Button Test | 9 June 2020 |
| YODA Conference | 22 June 2020 |
|  |  |

Division of Labour

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| --- | --- |
| **Task** | **Person** |
| Core function Code(Function Samples Generator) | Dylan |
| Seven Segment Output Integration | Sarvesh |
| Button/Switch Input Integration | Sarvesh |
| BRAM Navigation | Takudzwa |
| Upload & Test | Takudzwa |
| Memory Optimization | Dylan |
| Audio Output | Takudzwa |
|  |  |

**Initial Block diagram**

